

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s):	Brian T. Edgar	Examiner:	Loan Truong
Application No.:	10/823,225	Group Art Unit:	2114
Filed:	April 13, 2004	Docket No.:	STL11600
Title:	Simulated Error Injection System In Target Device For Testing Host System		
Confirmation No:	4622		

COMMUNICATION

Sir:

In response to the Notice of Allowance and Issue Fee communication mailed June 10, 2008, please consider the following remarks.

Remarks

Comments on Statement of Reasons for Allowance

The Applicant agrees with the Examiner's Statement of Reasons for Allowance to the extent that the claims of the present invention are patentable over the references in the record. The Applicant expressly traverses the Examiner's Statement of Reasons for Allowance to the extent that any comment is intended or has the effect of limiting a claim scope, explicitly or implicitly, by not reciting verbatim the respective claim language, or is intended or has the effect of limiting a claim scope by stating or implying that all the reasons for patentability are in any way fully enumerated.

Respectfully submitted,

Seagate Technology LLC
(Assignee of the Entire Interest)

June 25, 2008
Date

/David K. Lucente/
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